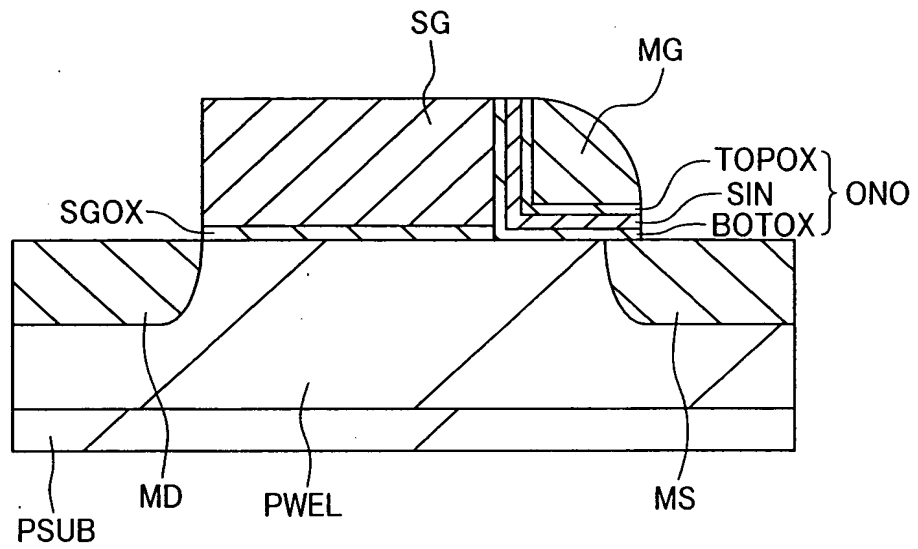


FIG.1



# FIG.2

|                                   | Vsg | Vmg | Vs  | Vd  | Vwell |                               |
|-----------------------------------|-----|-----|-----|-----|-------|-------------------------------|
| WRITE<br>(HOT ELECTRON INJECTION) | 0.7 | 10  | 6   | 0   | 0     | SET CHANNEL<br>CURRENT BY Vsg |
|                                   | 1.5 | 10  | 6   | 0.8 | 0     | SET CHANNEL<br>CURRENT BY Vd  |
| ERASE<br>(HOT HOLE INJECTION)     | 0.7 | -8  | 7   | 0   | 0     | SET CHANNEL<br>CURRENT BY Vsg |
|                                   | 1.5 | -8  | 7   | 0.8 | 0     | SET CHANNEL<br>CURRENT BY Vd  |
| READ                              | 1.5 | 1.5 | 0   | 1.5 | 0     | REVERSE                       |
|                                   | 1.5 | 1.5 | 1.5 | 0   | 0     | FORWARD                       |

FIG.3

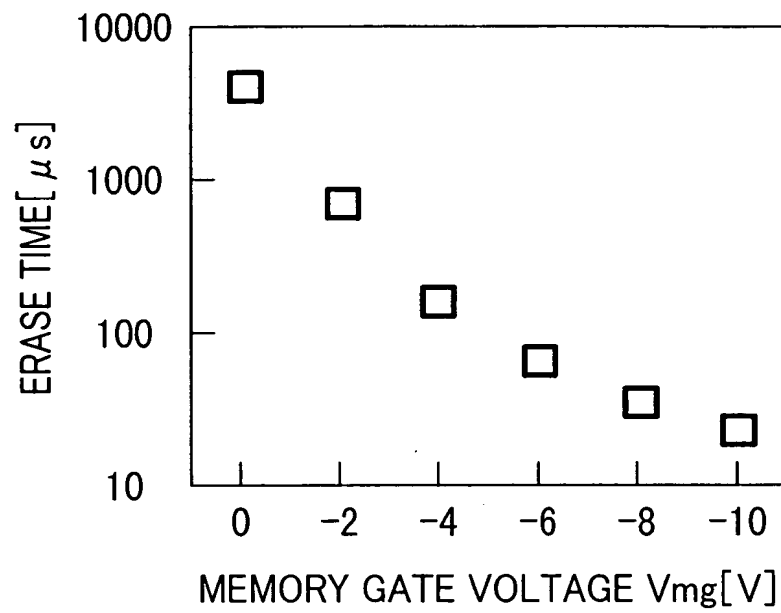


FIG.4

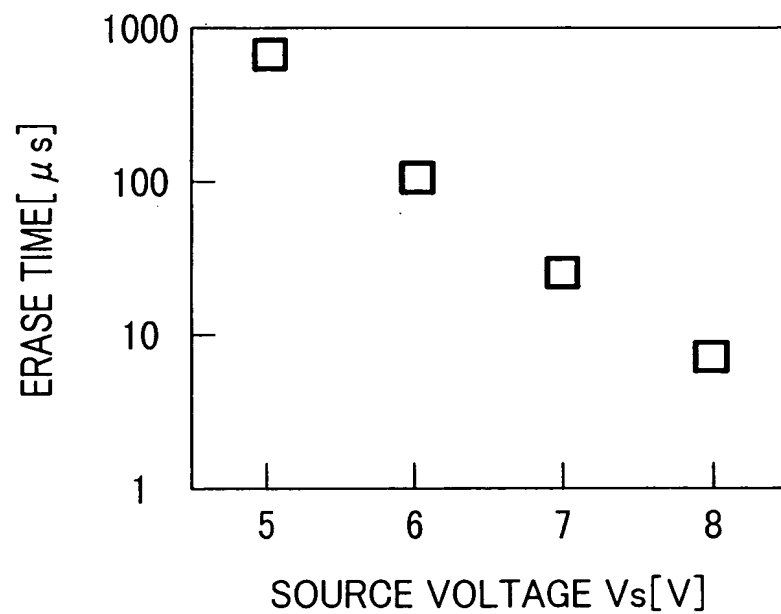
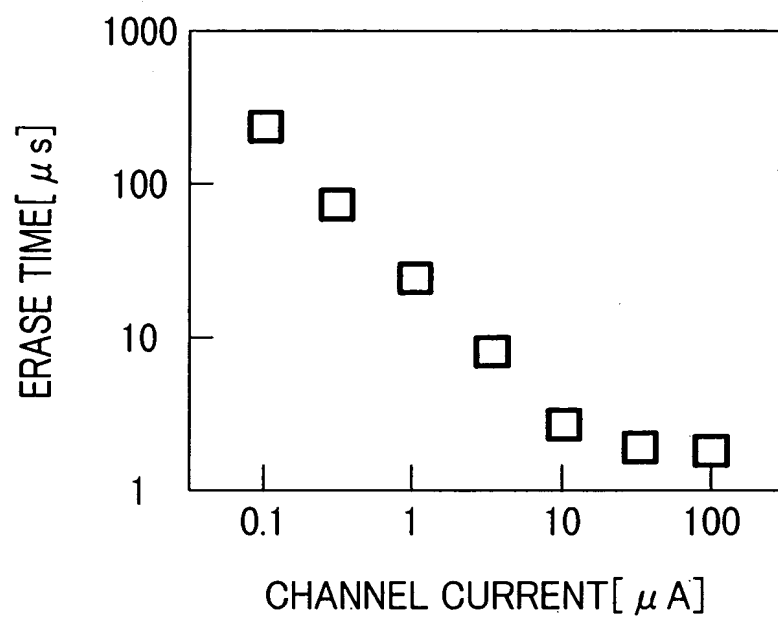
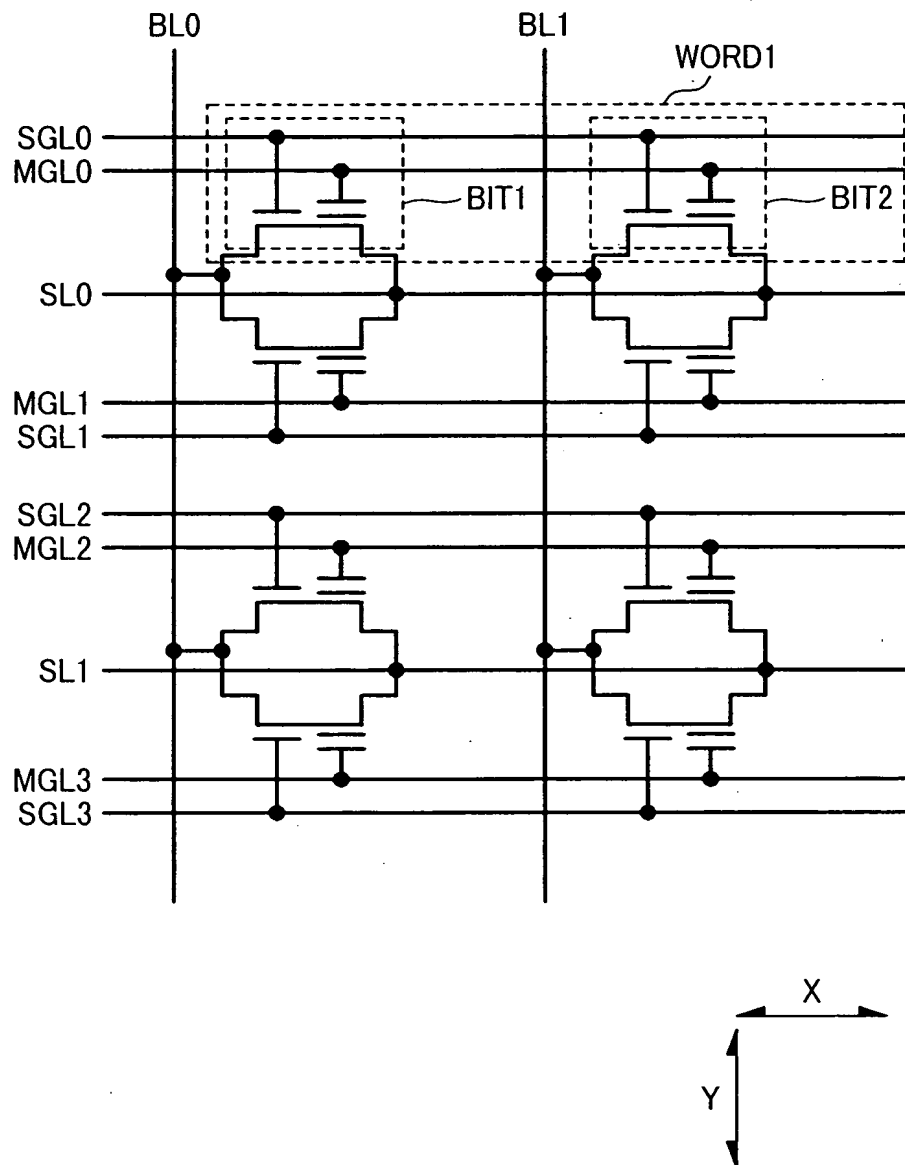


FIG.5



# FIG.6



# FIG.7

|       |             | SGL0 | SGL1~3 | MGL0 | MGL1~3 | SL0 | SL1 | BL0 | BL1 | SELECTED CELLS |
|-------|-------------|------|--------|------|--------|-----|-----|-----|-----|----------------|
| ERASE | WRITE       | 0.7  | 0      | 10   | 0      | 6   | 0   | 0   | 1.5 | BIT1           |
|       | BIT ERASE   | 0.7  | 0      | -8   | 0      | 7   | 0   | 0   | 1.5 | BIT1           |
|       | WORD ERASE  | 0.7  | 0      | -8   | 0      | 7   | 0   | 0   | 0   | WORD1          |
|       | BLOCK ERASE | 0.7  | 0.7    | -8   | -8     | 7   | 6   | 0   | 0   | ALL            |
| READ  | REVERSE     | 1.5  | 0      | 1.5  | 0      | 0   | 0   | 1.5 | 0   | BIT1           |
|       | FORWARD     | 1.5  | 0      | 1.5  | 0      | 1.5 | 0   | 0   | 1.5 | BIT1           |

# FIG.8

|       |             | SGL0 | SGL1~3 | MGL0 | MGL1~3 | SL0 | SL1 | BL0 | BL1 | SELECTED CELLS |
|-------|-------------|------|--------|------|--------|-----|-----|-----|-----|----------------|
| ERASE | WRITE       | 1.5  | 0      | 10   | 0      | 6   | 0   | 0.8 | 1.5 | BIT1           |
|       | BIT ERASE   | 1.5  | 0      | -8   | 0      | 7   | 0   | 0.8 | 1.5 | BIT1           |
|       | WORD ERASE  | 1.5  | 0      | -8   | 0      | 7   | 0   | 0.8 | 0.8 | WORD1          |
|       | BLOCK ERASE | 1.5  | 1.5    | -8   | -8     | 7   | 6   | 0.8 | 0.8 | ALL            |
| READ  | REVERSE     | 1.5  | 0      | 1.5  | 0      | 0   | 0   | 1.5 | 0   | BIT1           |
|       | FORWARD     | 1.5  | 0      | 1.5  | 0      | 1.5 | 0   | 0   | 1.5 | BIT1           |

# FIG.9

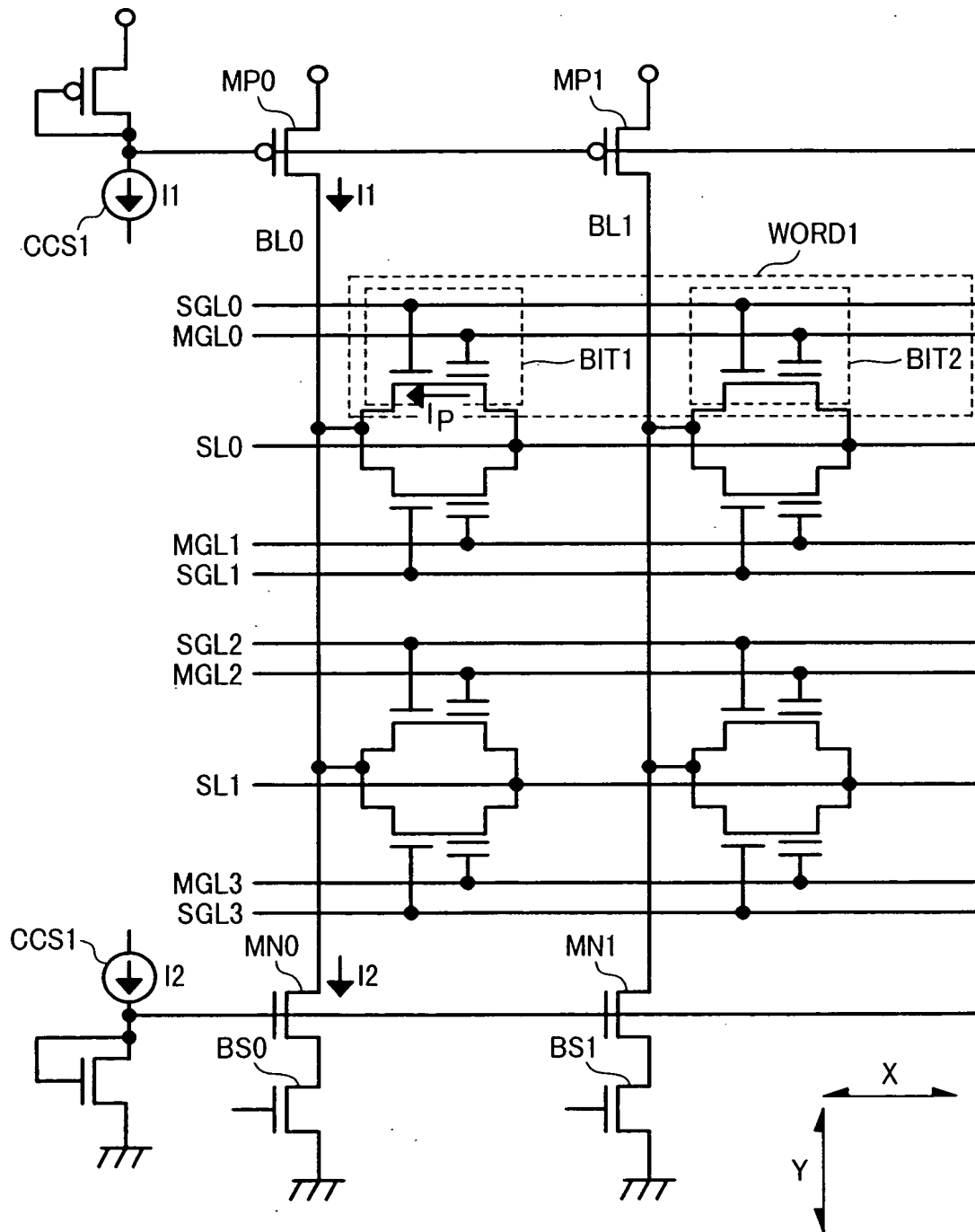
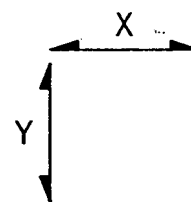
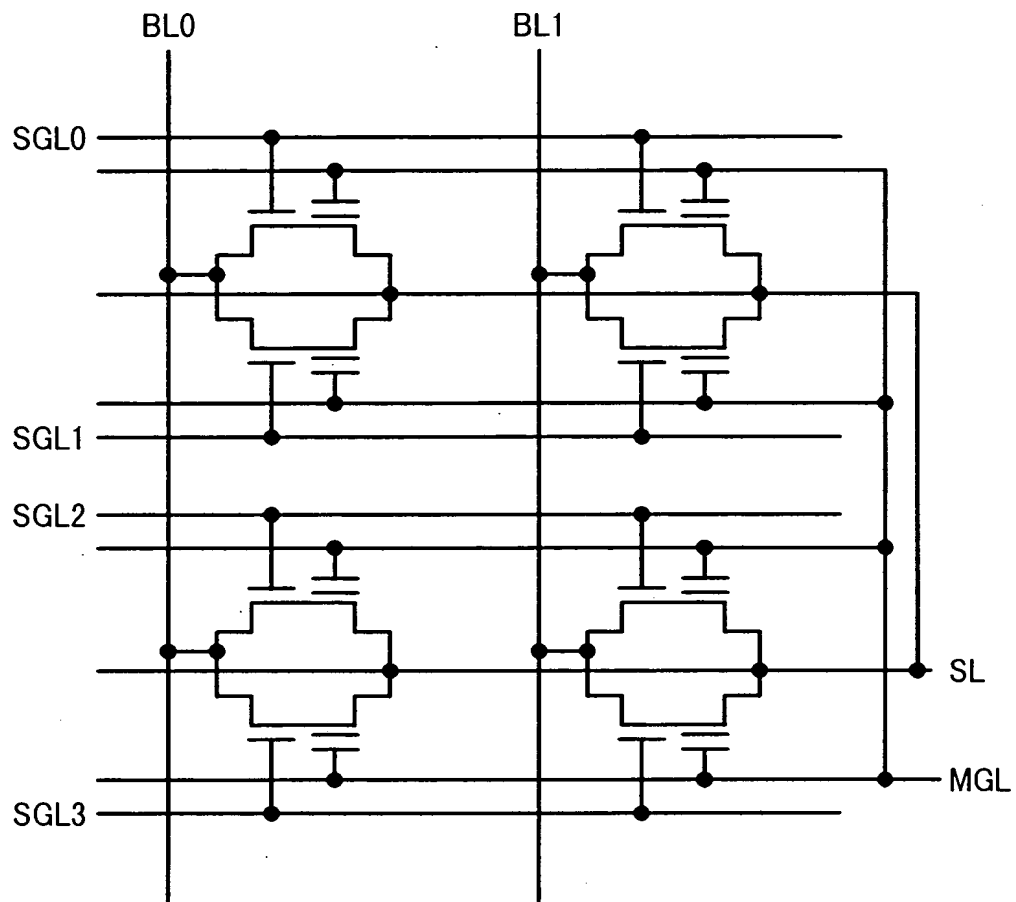


FIG.10





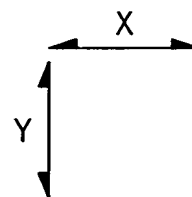
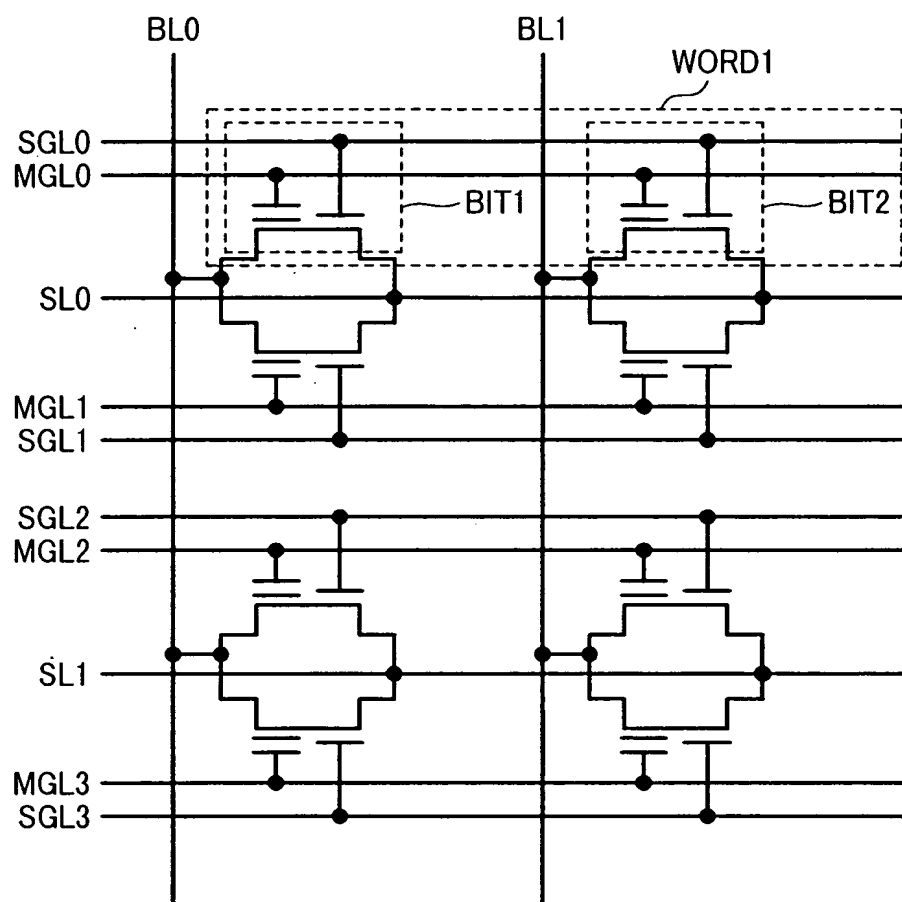
# FIG.11

|       |             | SGL0 | SGL1~3 | MGL | SL  | BL0 | BL1 | SELECTED CELLS |
|-------|-------------|------|--------|-----|-----|-----|-----|----------------|
| ERASE | WRITE       | 0.7  | 0      | 10  | 6   | 0   | 1.5 | BIT1           |
|       | BIT ERASE   | 0.7  | 0      | -8  | 7   | 0   | 1.5 | BIT1           |
|       | WORD ERASE  | 0.7  | 0      | -8  | 7   | 0   | 0   | WORD1          |
|       | BLOCK ERASE | 0.7  | 0.7    | -8  | 7   | 0   | 0   | ALL            |
| READ  | REVERSE     | 1.5  | 0      | 1.5 | 0   | 1.5 | 0   | BIT1           |
|       | FORWARD     | 1.5  | 0      | 1.5 | 1.5 | 0   | 1.5 | BIT1           |

# FIG.12

|       |             | SGL0 | SGL1~3 | MGL | SL  | BL0 | BL1 | SELECTED CELLS |
|-------|-------------|------|--------|-----|-----|-----|-----|----------------|
| ERASE | WRITE       | 1.5  | 0      | 10  | 6   | 0.8 | 1.5 | BIT1           |
|       | BIT ERASE   | 1.5  | 0      | -8  | 7   | 0.8 | 1.5 | BIT1           |
|       | WORD ERASE  | 1.5  | 0      | -8  | 7   | 0.8 | 0.8 | WORD1          |
|       | BLOCK ERASE | 1.5  | 1.5    | -8  | 7   | 0.8 | 0.8 | ALL            |
| READ  | REVERSE     | 1.5  | 0      | 1.5 | 0   | 1.5 | 0   | BIT1           |
|       | FORWARD     | 1.5  | 0      | 1.5 | 1.5 | 0   | 1.5 | BIT1           |

FIG.13



# FIG.14

|       |             | SGL0 | SGL1~3 | MGL0 | MGL1~3 | SL0 | SL1 | BL0 | BL1 | SELECTED CELLS |
|-------|-------------|------|--------|------|--------|-----|-----|-----|-----|----------------|
|       | WRITE       | 0.7  | 0      | 10   | 0      | 0   | 1.5 | 6   | 0.8 | BIT1           |
|       | BIT ERASE   | 0.7  | 0      | -8   | 0      | 0   | 1.5 | 7   | 0.8 | BIT1           |
| ERASE | WORD ERASE  | 0.7  | 0      | -8   | 0      | 0   | 1.5 | 7   | 7   | WORD1          |
|       | BLOCK ERASE | 0.7  | 0.7    | -8   | -8     | 0   | 0   | 7   | 7   | ALL            |
| READ  | REVERSE     | 1.5  | 0      | 1.5  | 0      | 1.5 | 0   | 0   | 1.5 | BIT1           |
|       | FORWARD     | 1.5  | 0      | 1.5  | 0      | 0   | 0   | 1.5 | 0   | BIT1           |

# FIG.15

|       |             | SGL0 | SGL1~3 | MGL0 | MGL1~3 | SL0 | SL1 | BL0 | BL1 | SELECTED CELLS |
|-------|-------------|------|--------|------|--------|-----|-----|-----|-----|----------------|
|       | WRITE       | 1.5  | 0      | 10   | 0      | 0.8 | 1.5 | 6   | 0.8 | BIT1           |
|       | BIT ERASE   | 1.5  | 0      | -8   | 0      | 0.8 | 1.5 | 7   | 0.8 | BIT1           |
| ERASE | WORD ERASE  | 1.5  | 0      | -8   | 0      | 0.8 | 1.5 | 7   | 7   | WORD1          |
|       | BLOCK ERASE | 1.5  | 1.5    | -8   | -8     | 0.8 | 0.8 | 7   | 7   | ALL            |
| READ  | REVERSE     | 1.5  | 0      | 1.5  | 0      | 1.5 | 0   | 0   | 1.5 | BIT1           |
|       | FORWARD     | 1.5  | 0      | 1.5  | 0      | 0   | 0   | 1.5 | 0   | BIT1           |

FIG.16

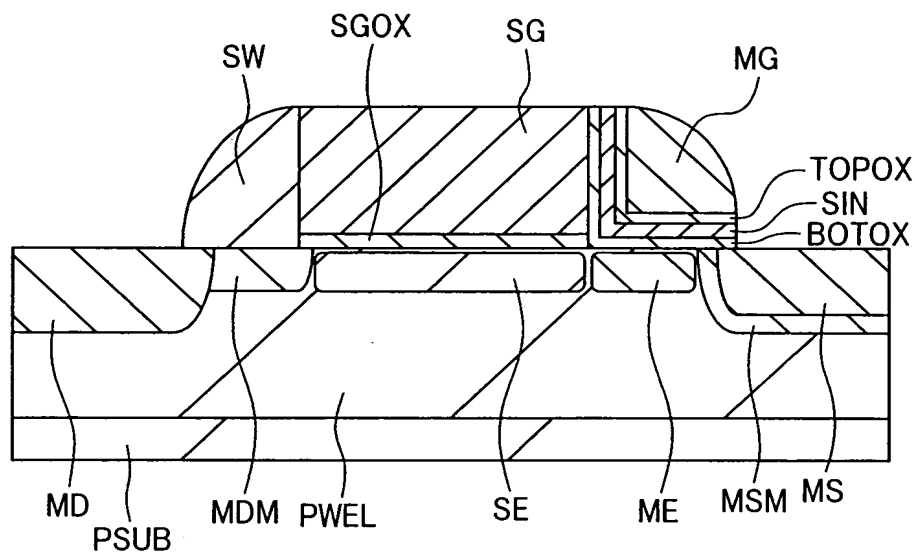


FIG.17

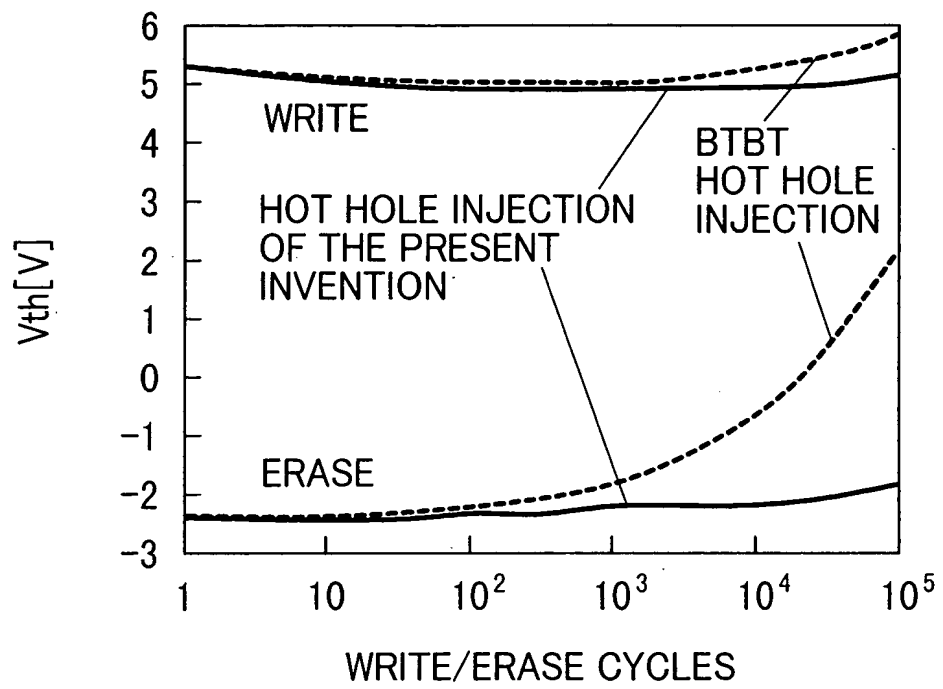


FIG.18

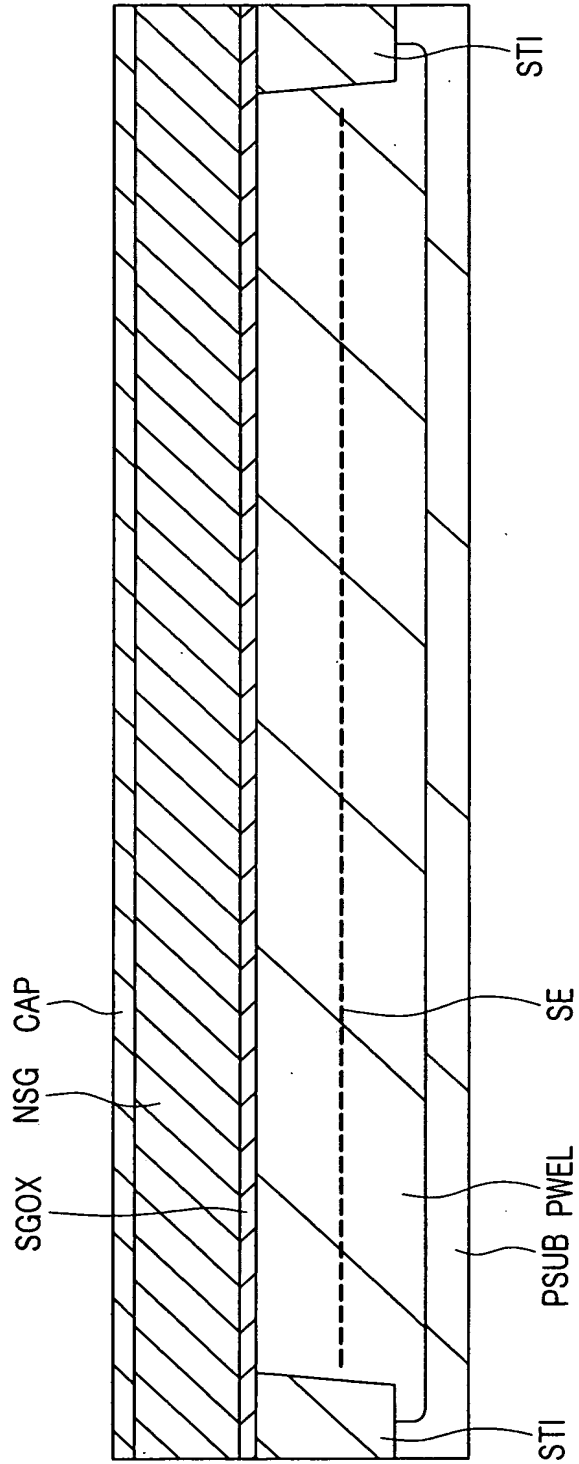


FIG.19

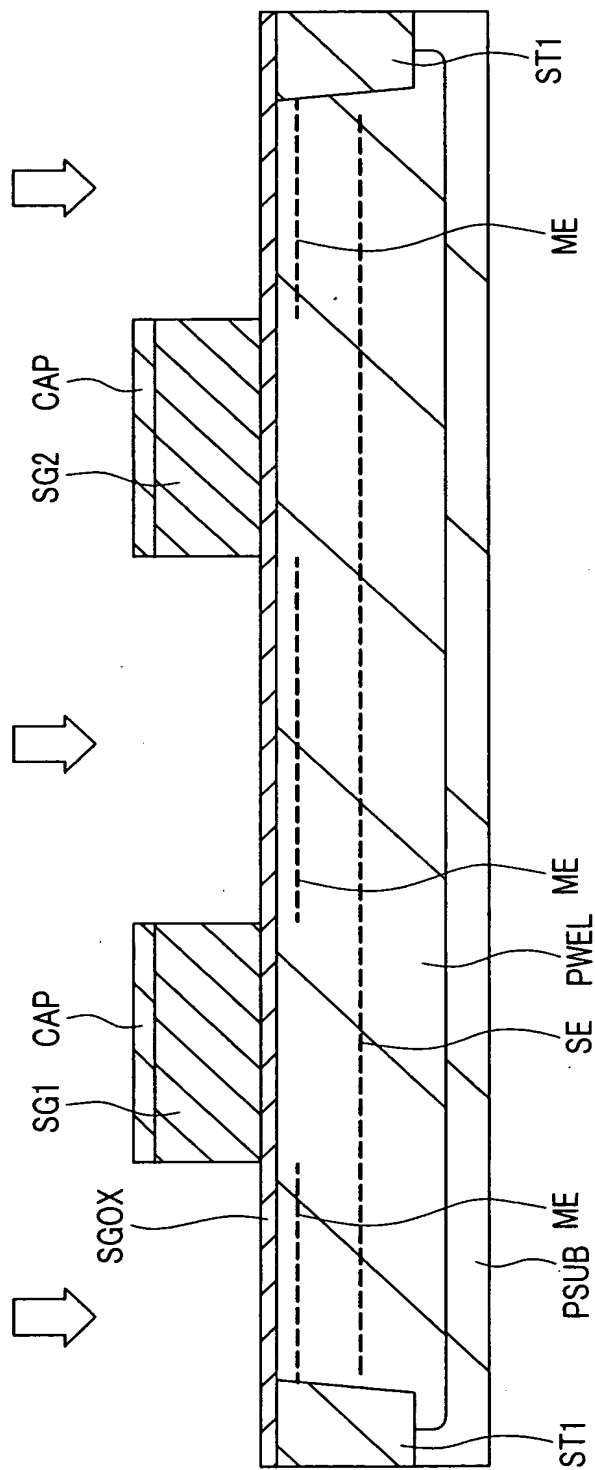


FIG.20

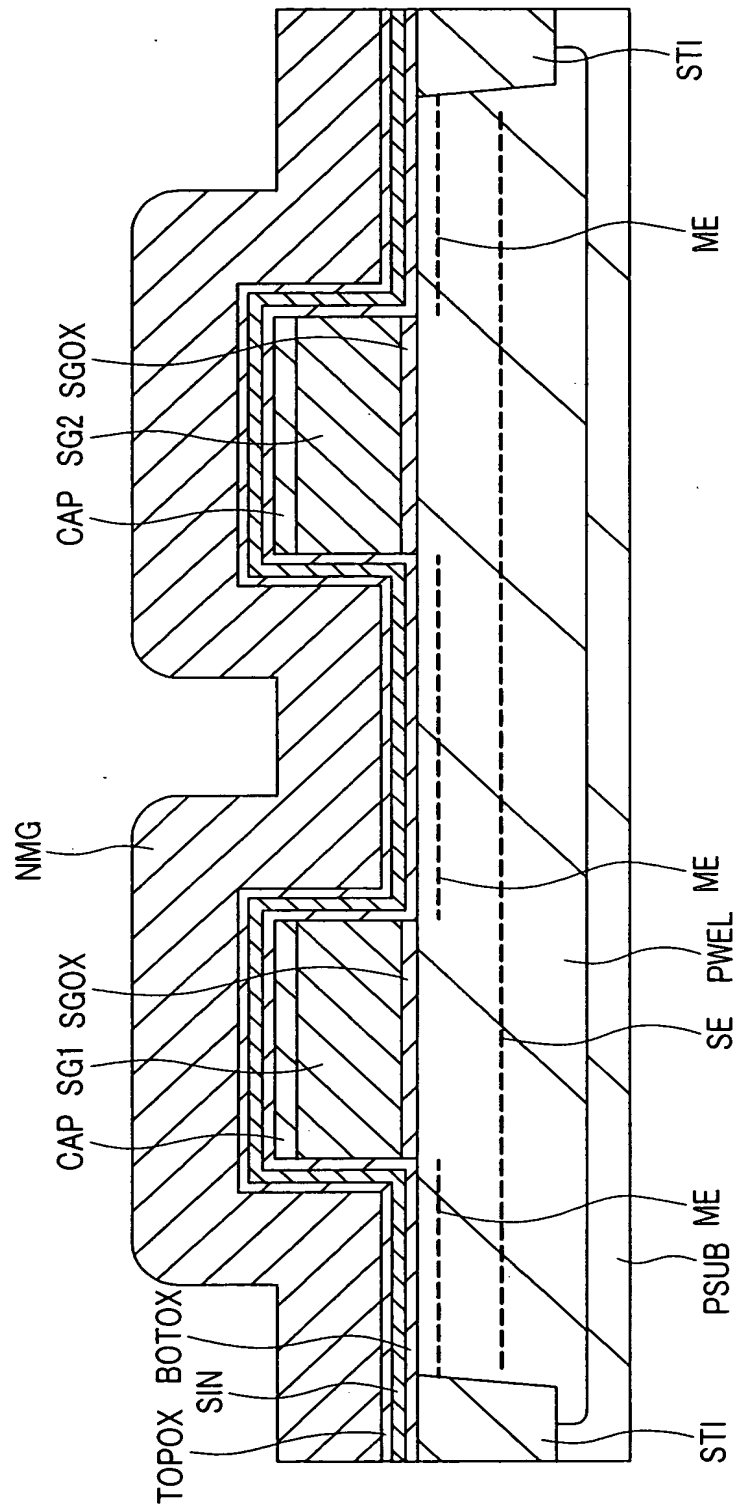
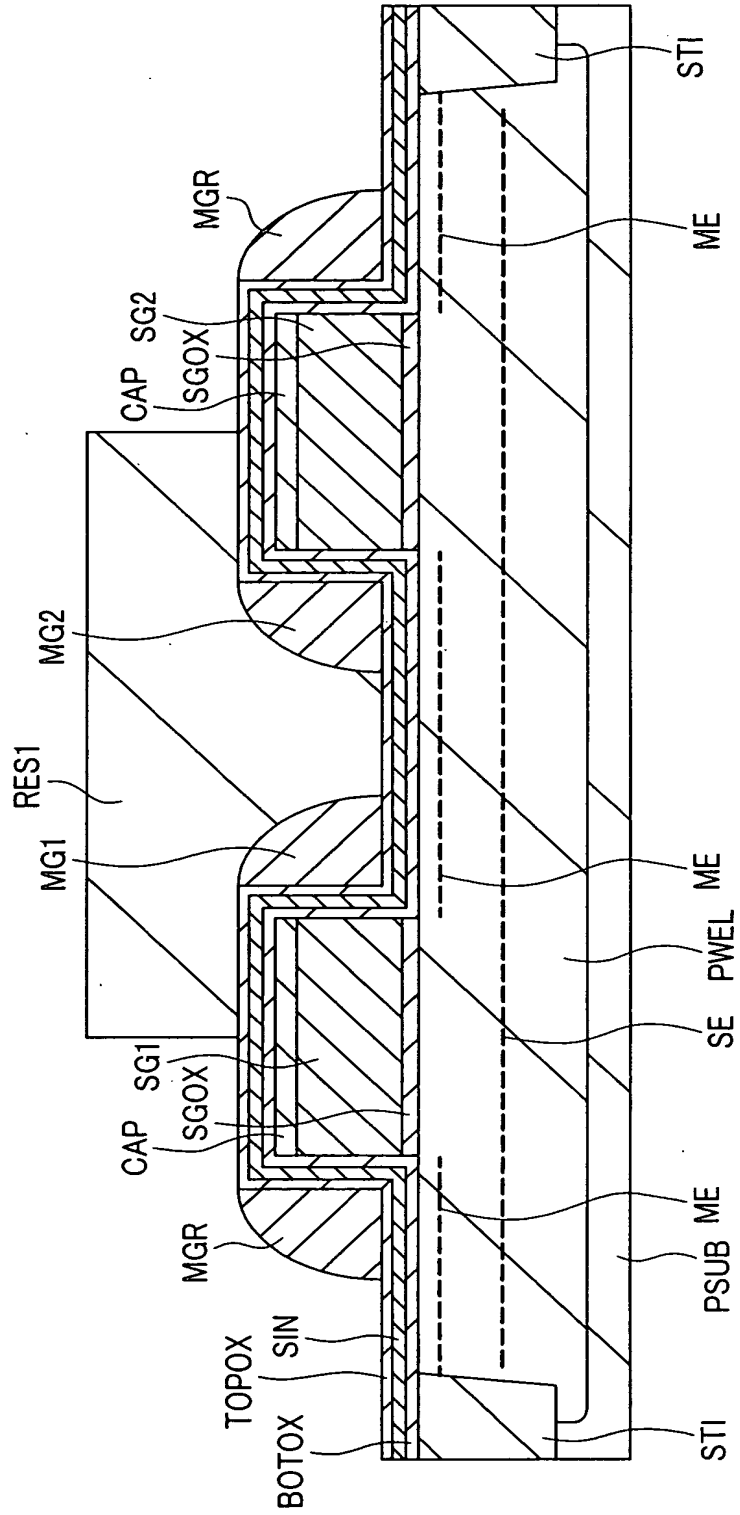
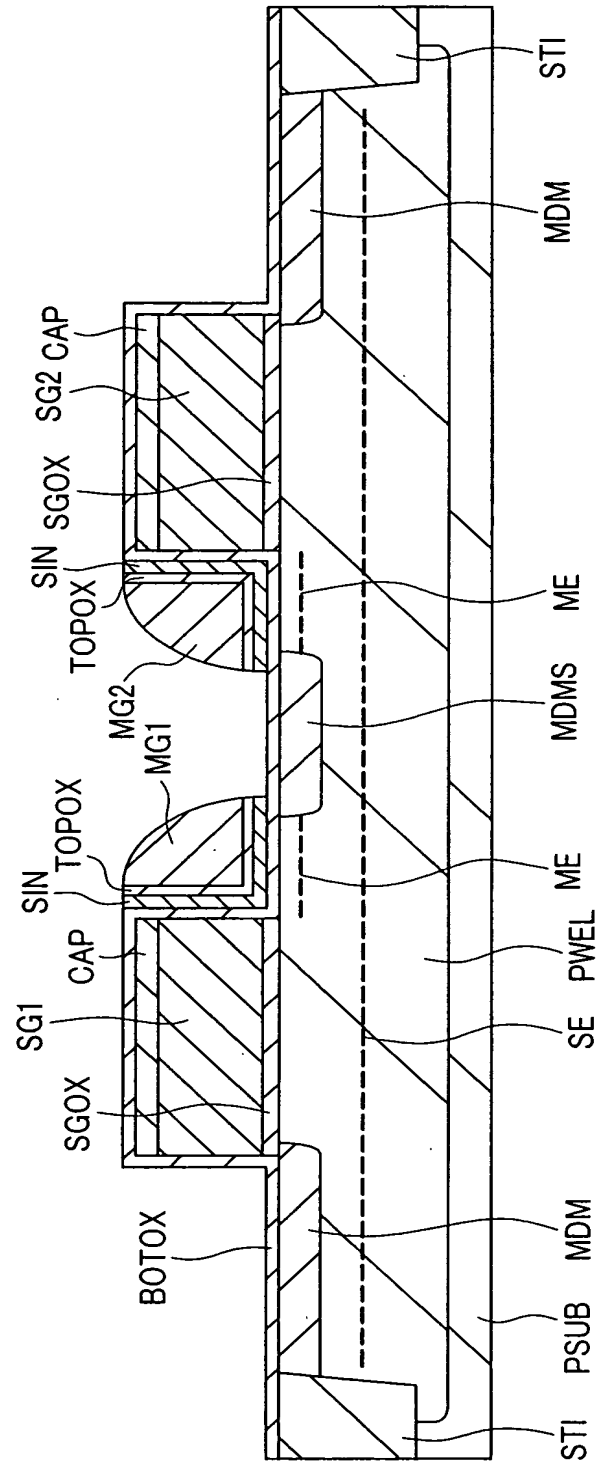




FIG.21



# FIG.22



# FIG.23

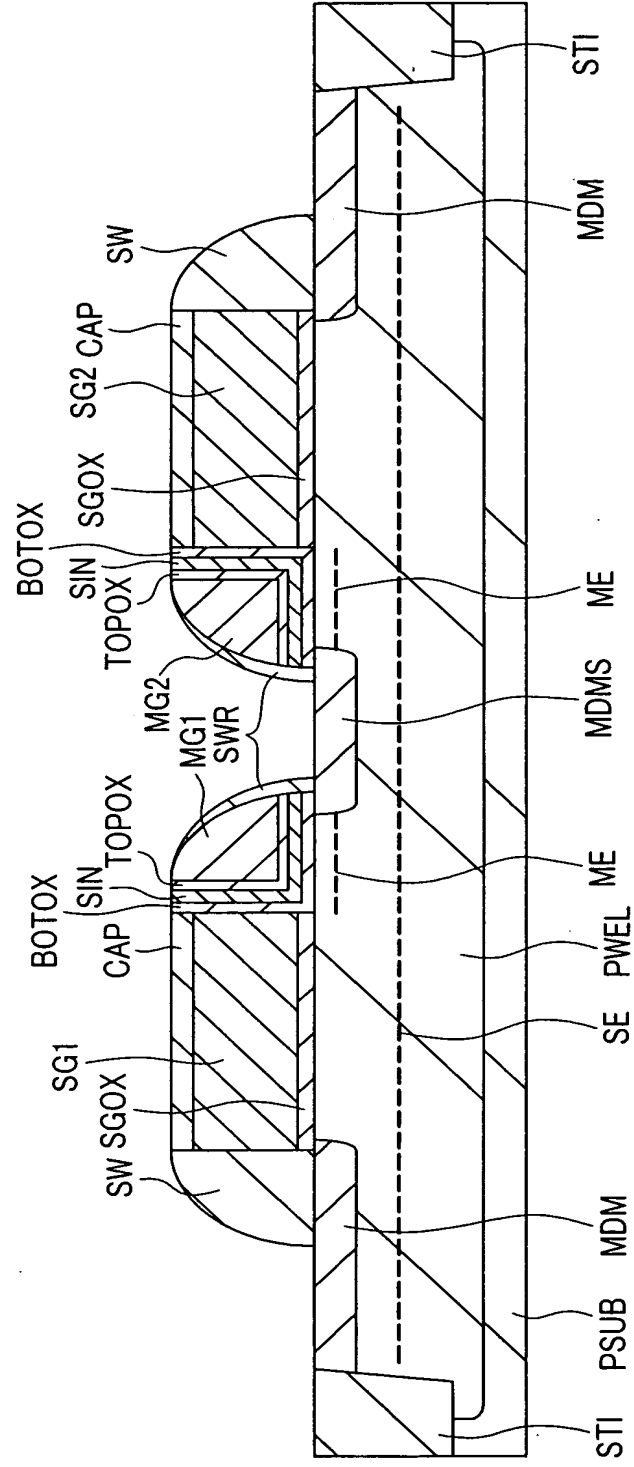


FIG. 24

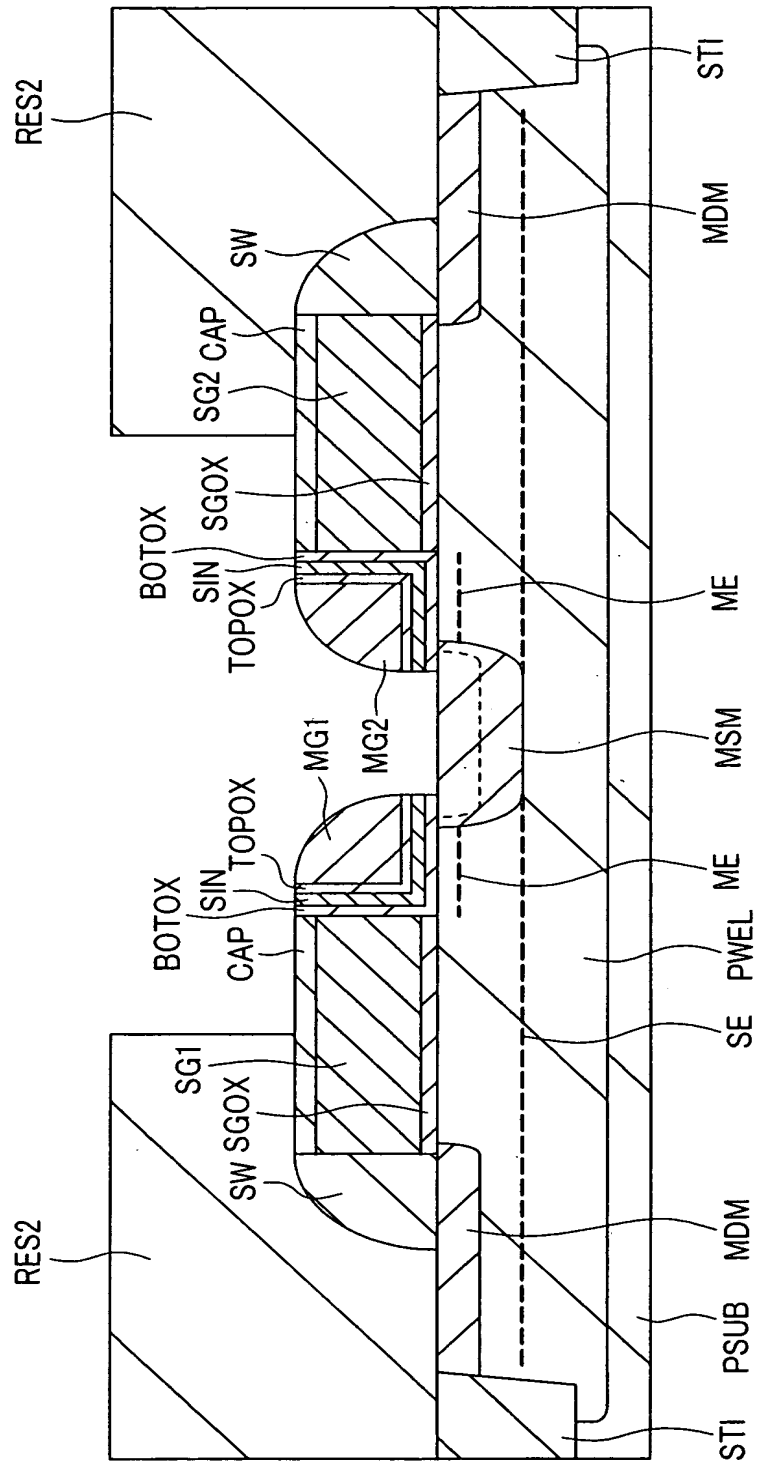
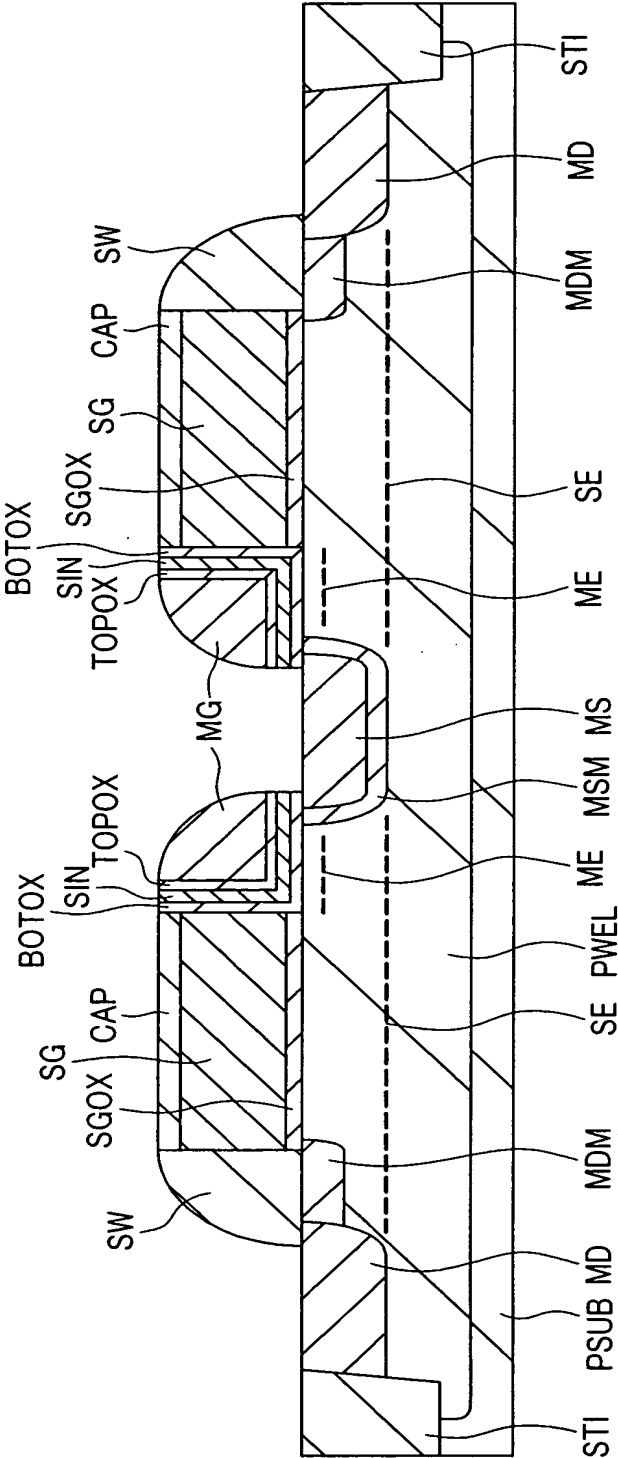


FIG.25



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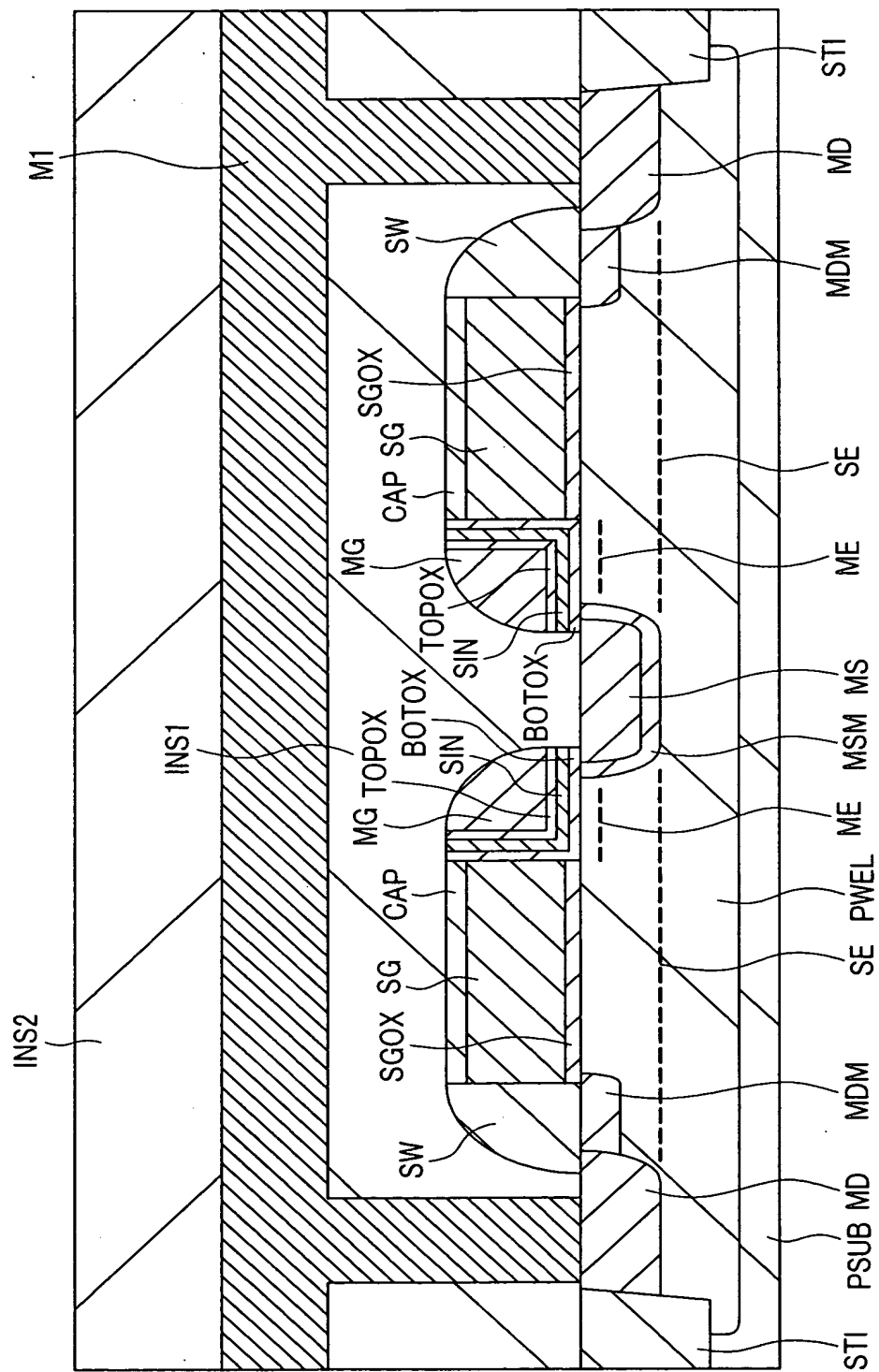
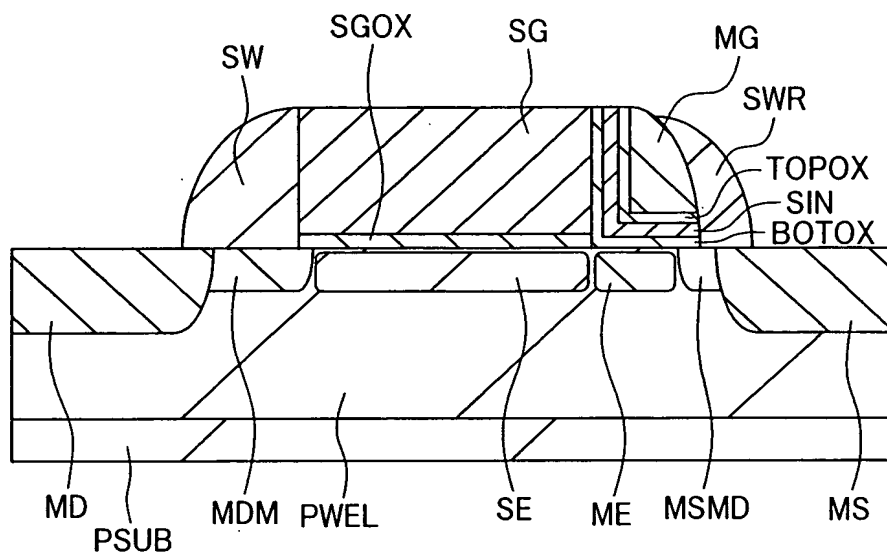
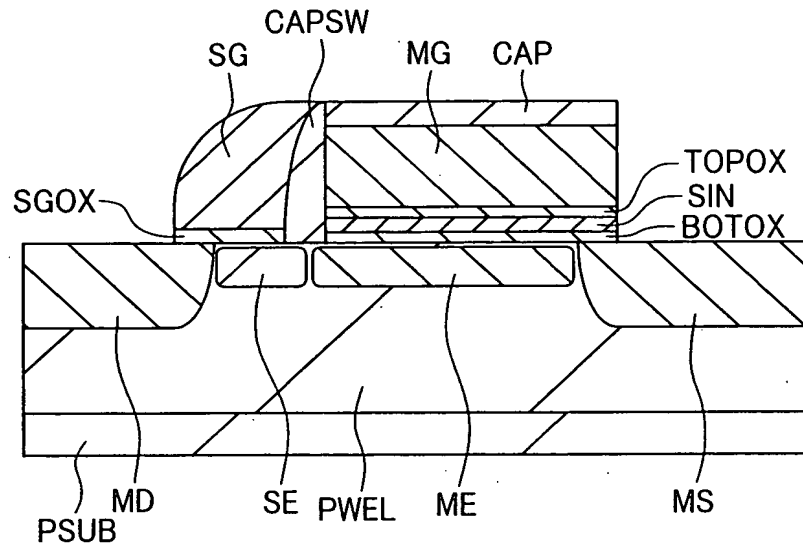


FIG.27



# FIG.28



# FIG.29

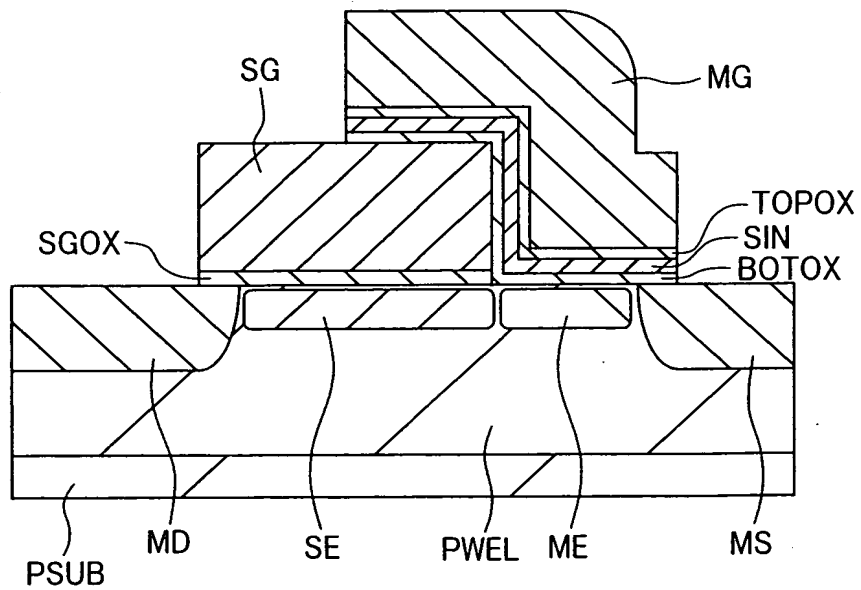




FIG.30

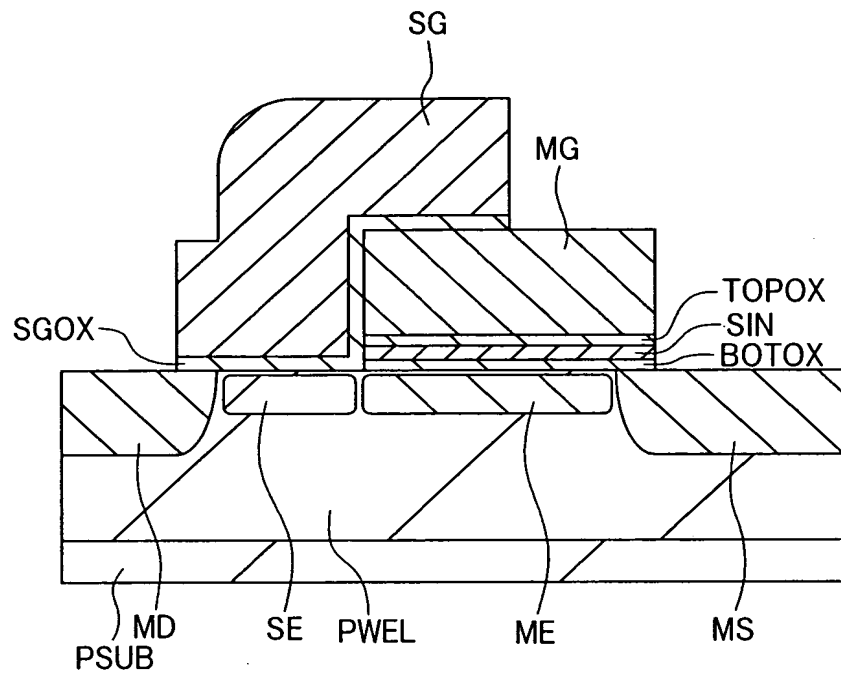


FIG.31

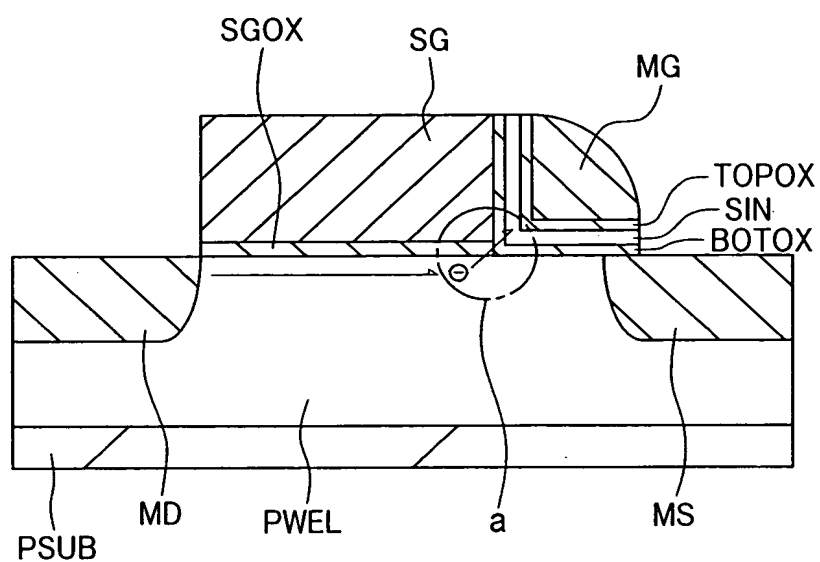
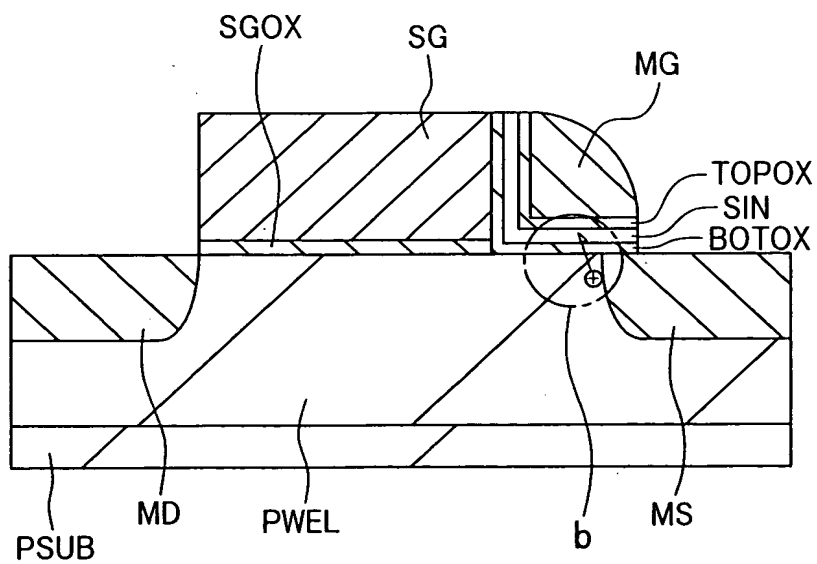


FIG.32



# FIG.33

